

# SPECIFICATION

**MODEL: B12010-Quad SPI**

**PART NO:** \_\_\_\_\_

**VERSION:** V1.02

Approver		Check	Design
GM	PM		

Customer Confirm

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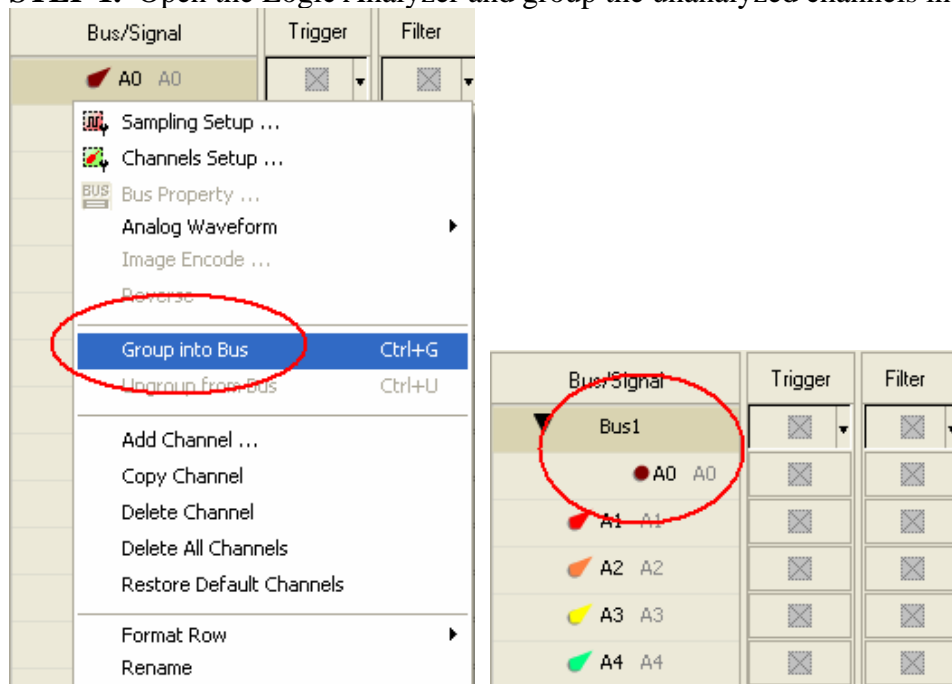
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# 1 Software Register

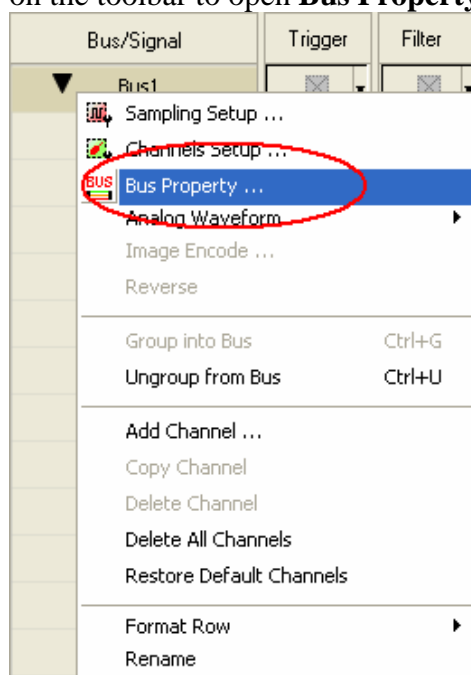
Please register the software as the following steps:

- ※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.
- ※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

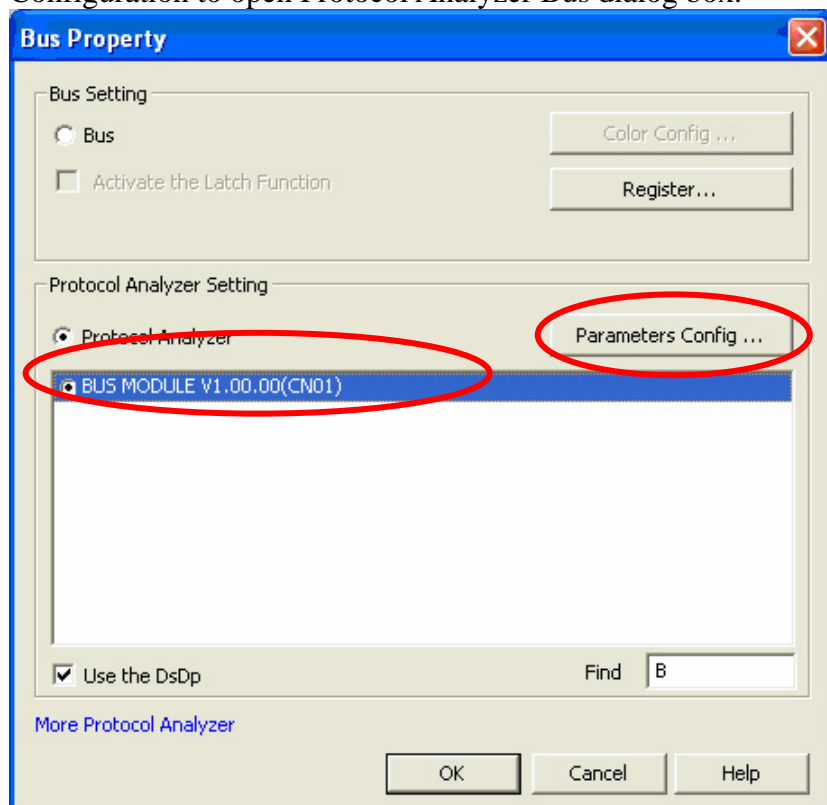
**STEP 1.** Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key**.



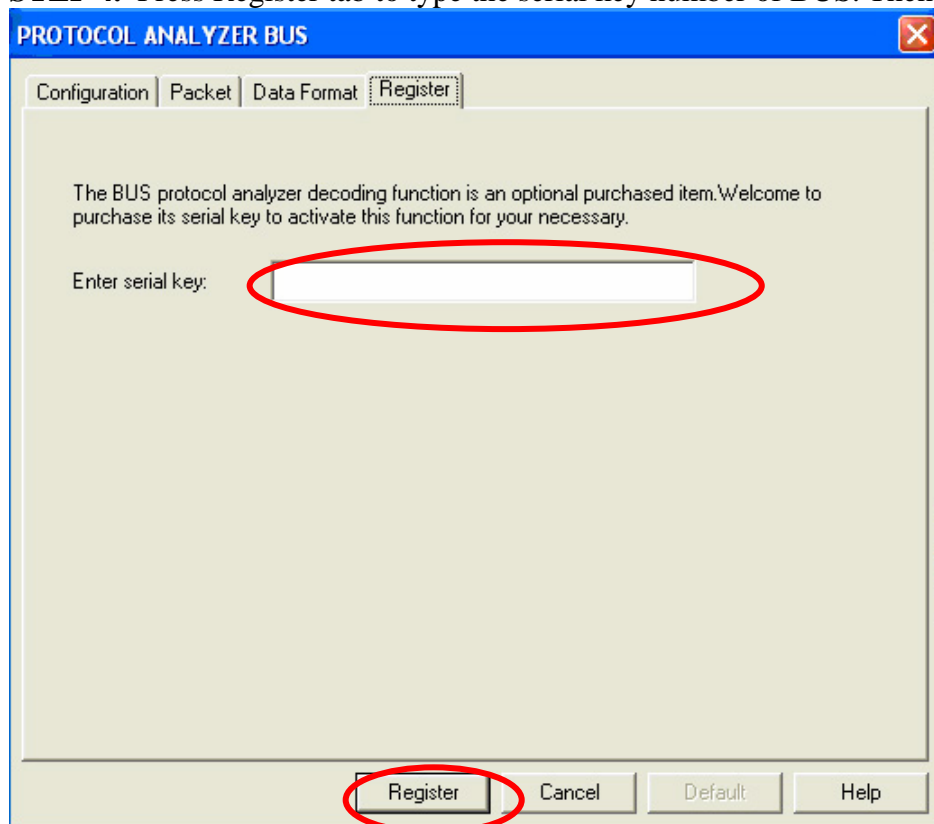
**STEP 2.** Select **Bus 1**, then press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.



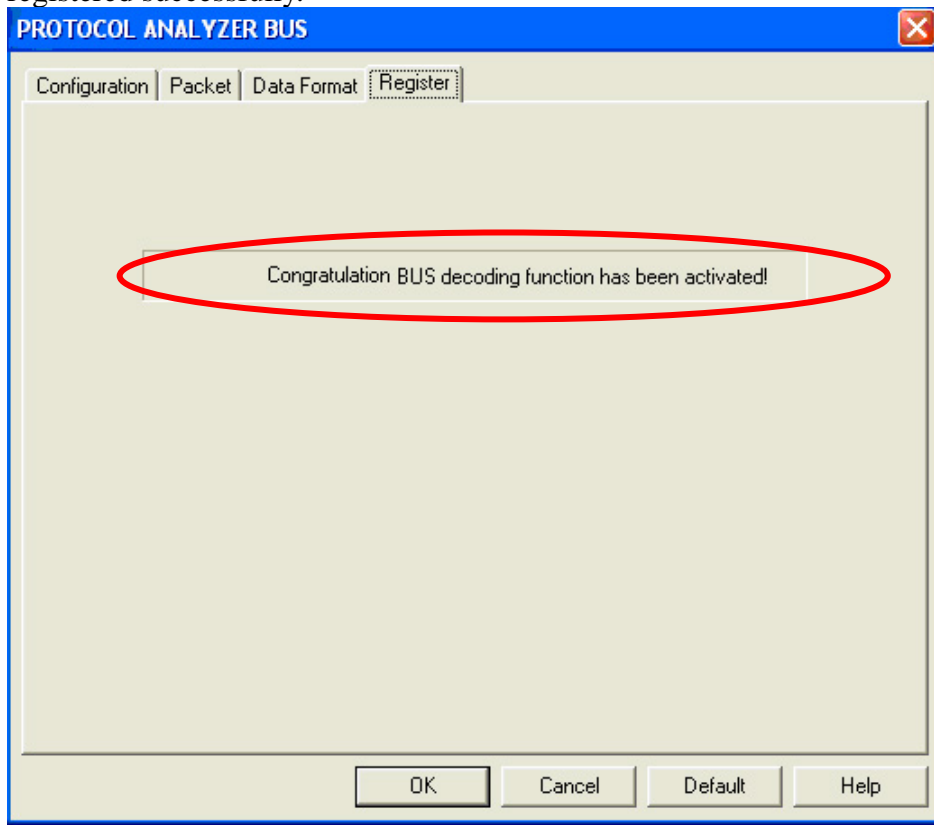
**STEP 3.** Select the Protocol Analyzer, and then choose **BUS MODULE V1.00.00 (CN01)**. Next click Parameters Configuration to open Protocol Analyzer Bus dialog box.



**STEP 4.** Press Register tab to type the serial key number of BUS. Then press Register.



**STEP 5.** After pressing the Register button, following dialog box will appear, it denotes that the BUS has been registered successfully.

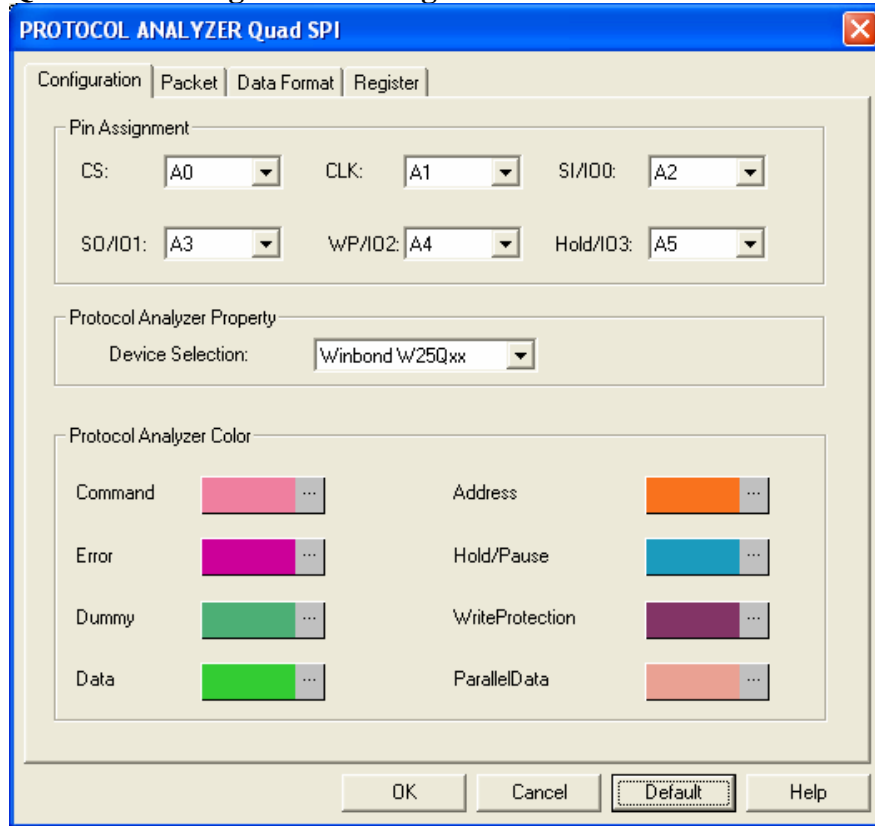


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## 2 User Interface

Please refer to the below images to do settings of Quad SPI module.

### Quad SPI Configuration dialog box



#### Pin Assignment:

Protocol Analyzer Quad SPI needs 6 lines to decode. CS is the line of chip selection, which is A0 by default; CLK is the clock line, which is A1 by default; SI/IO0 is the input line, which is A2 by default; SO/IO1 is the output line, which is A3 by default; WP/IO2 is the line of write protection, which is A4 by default; Hold/IO3 is the hold line, which is A5 by default.

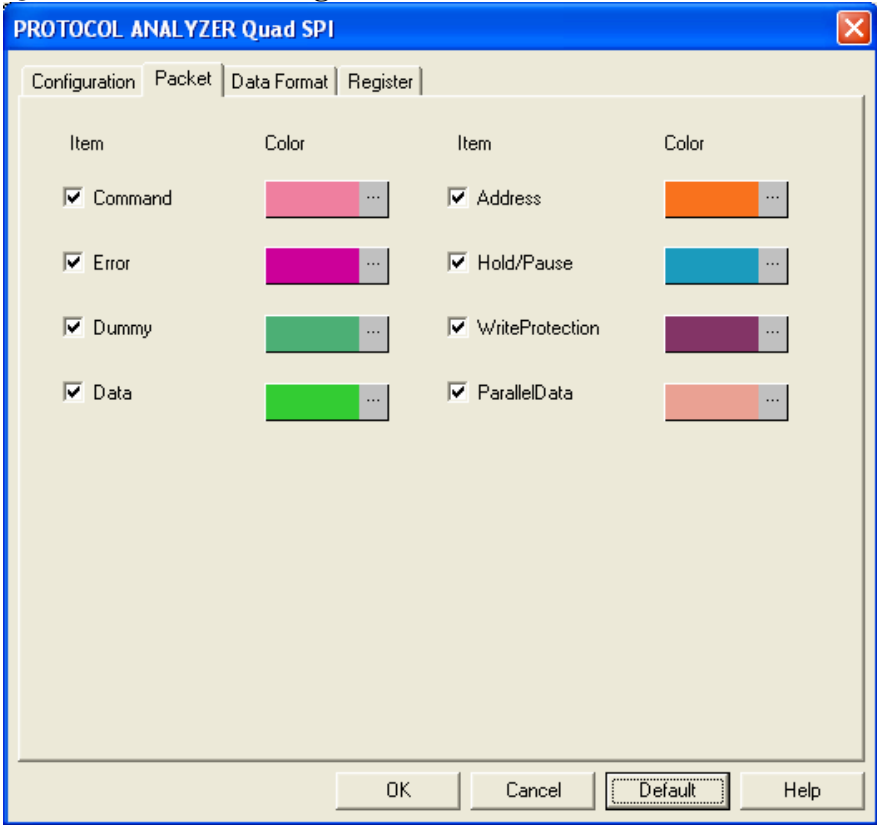
#### Device Selection:

There are Winbond W25Qxx, Atmel AT25DQxx1, MXIC MX25Lxx and SPANSION S25FLxx. It is Winbond W25Qxx by default.

#### Protocol Analyzer Color

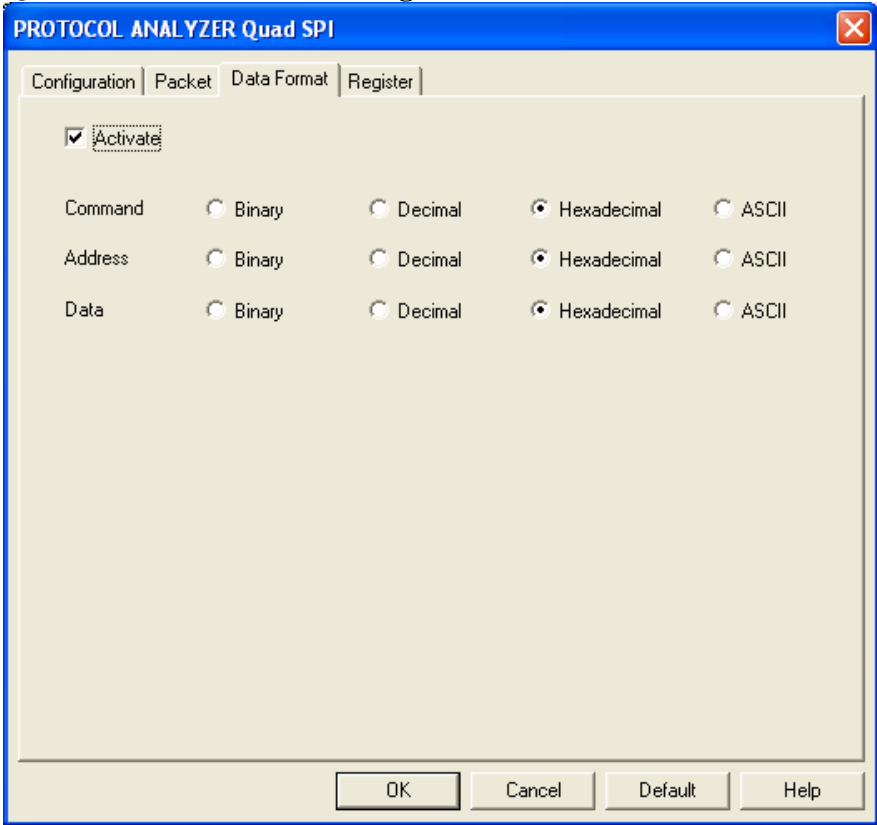
The color can be varied by users.

Quad SPI Packet dialog box



In the Packet part, users can select the items to be displayed and their colors as their requirements.

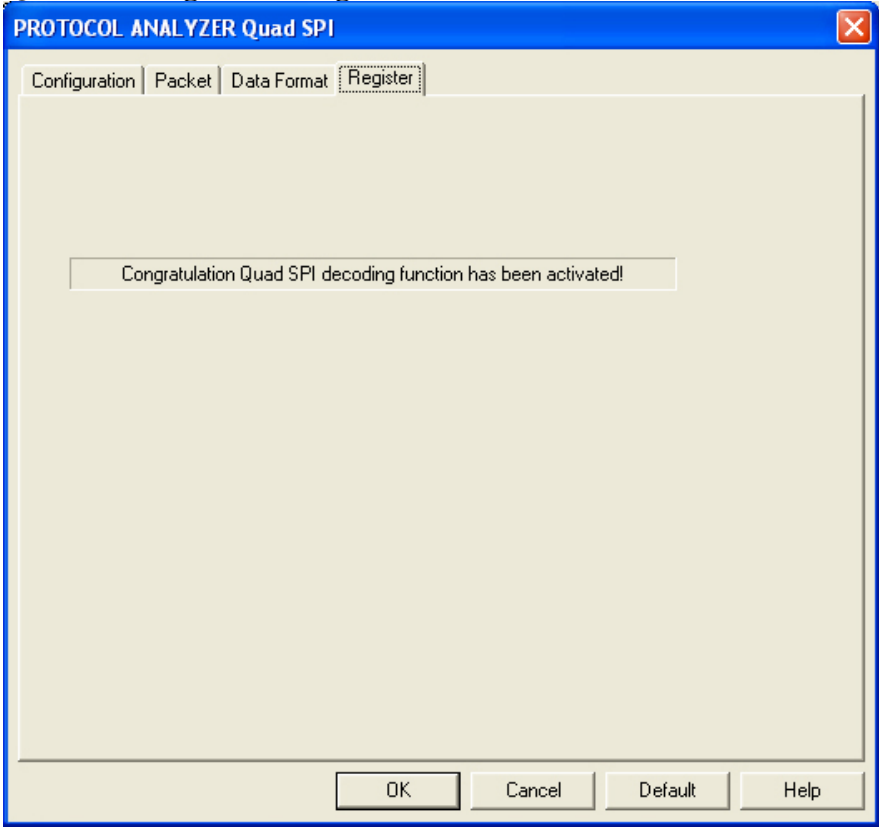
Quad SPI Data Format dialog box



Users can set the Command, Address and Data as their requirements. When selecting the option Activate, the data format is decided by the settings in the Protocol Analyzer; when not selecting the option Activate, the data format

is decided by the settings in the main program.

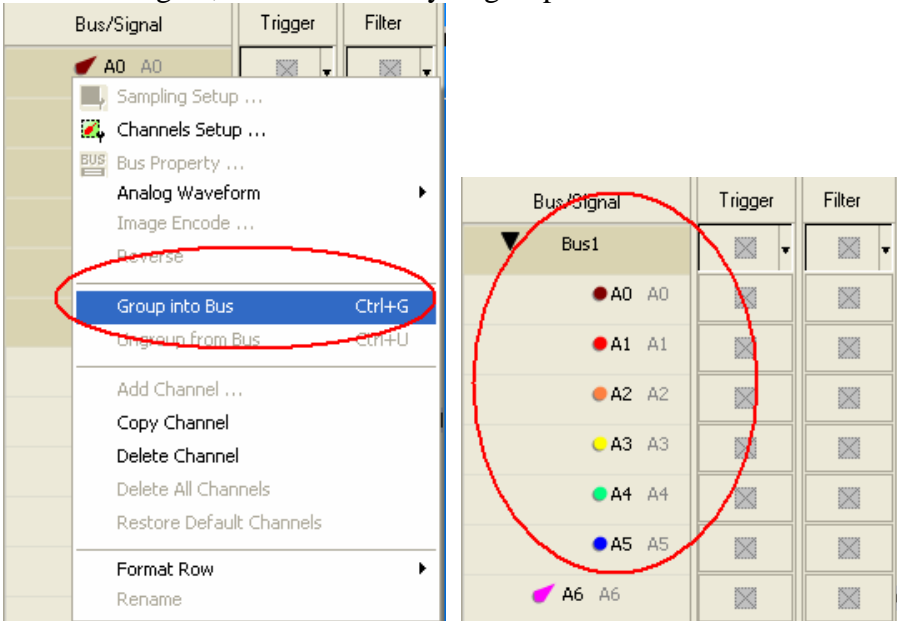
**Quad SPI Register dialog box**



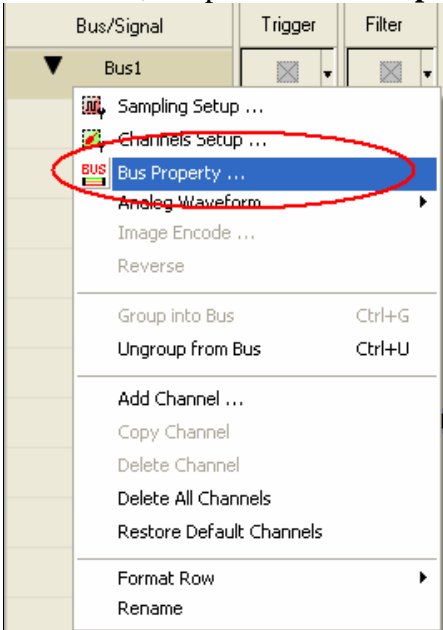


### 3 Operating Instructions

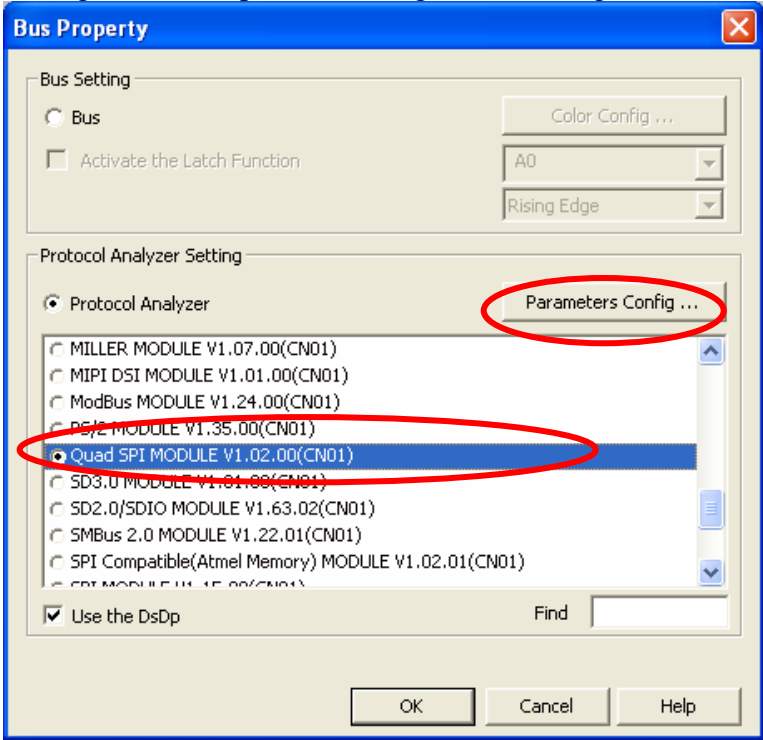
**STEP 1.** Group A0-A5 into **Bus1** by pressing the **Right Key** on the mouse. Quad SPI needs at least six channels to decode signal, so it is necessary to group six or more channels into the Bus.



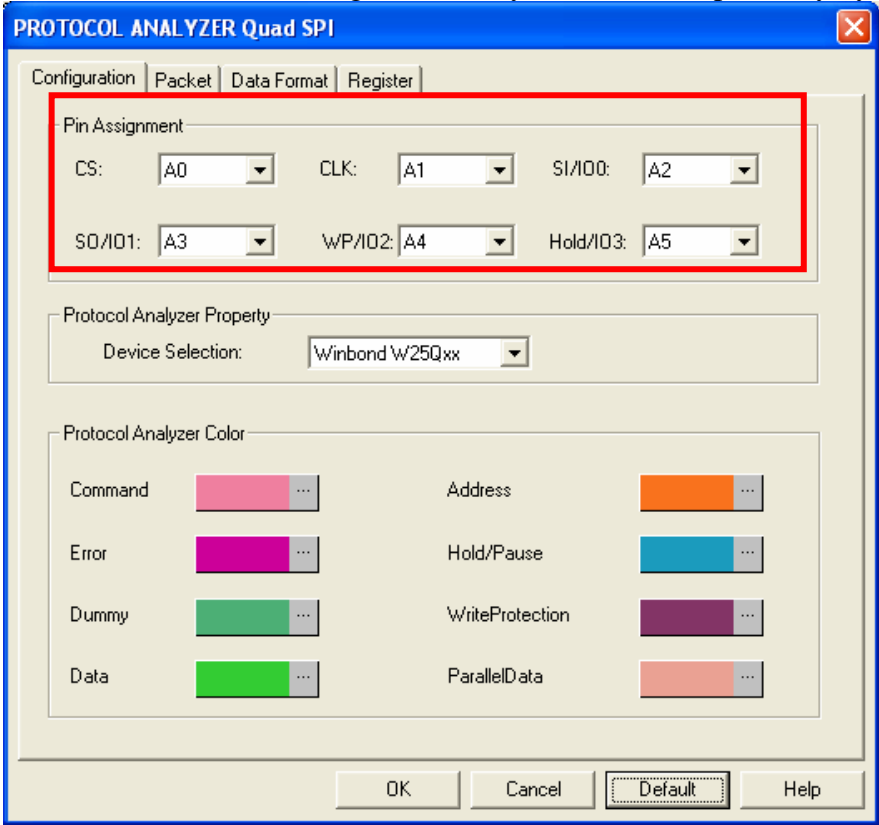
**STEP 2.** Select **Bus1**, press right key and select **Bus Property** from the popped menu, or click the **Bus** icon on the toolbar, to open the **Bus Property** dialog box.



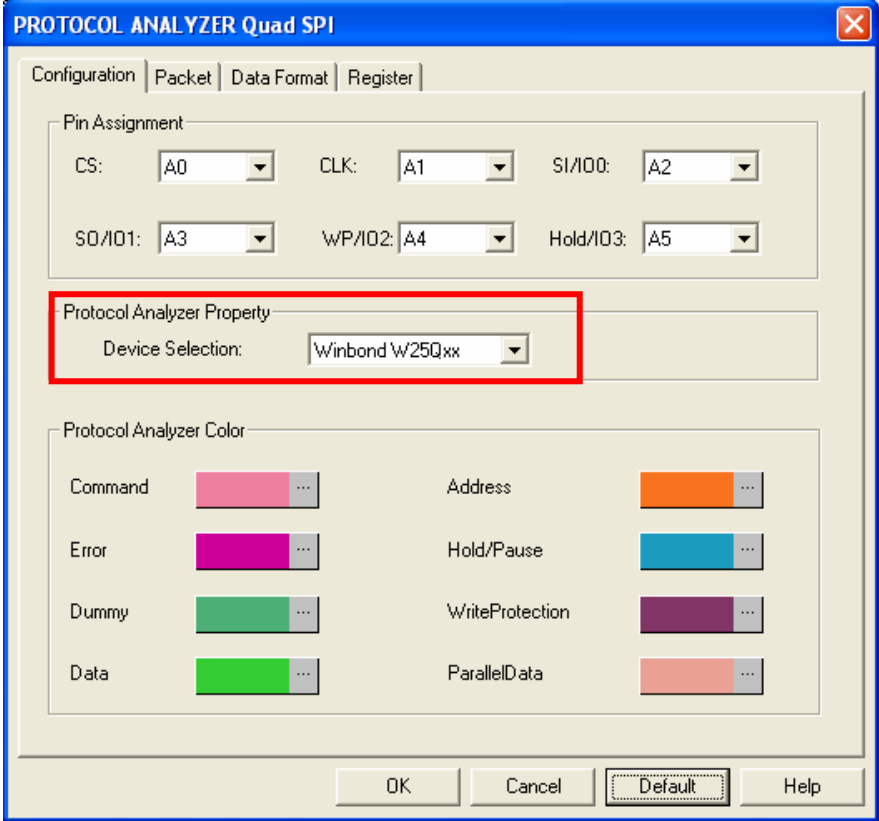
**STEP 3.** Select Protocol Analyzer, and select Quad-SPI MODULE V1.02.00(CN01), Then click Parameters Configuration to open the Configuration dialog box.



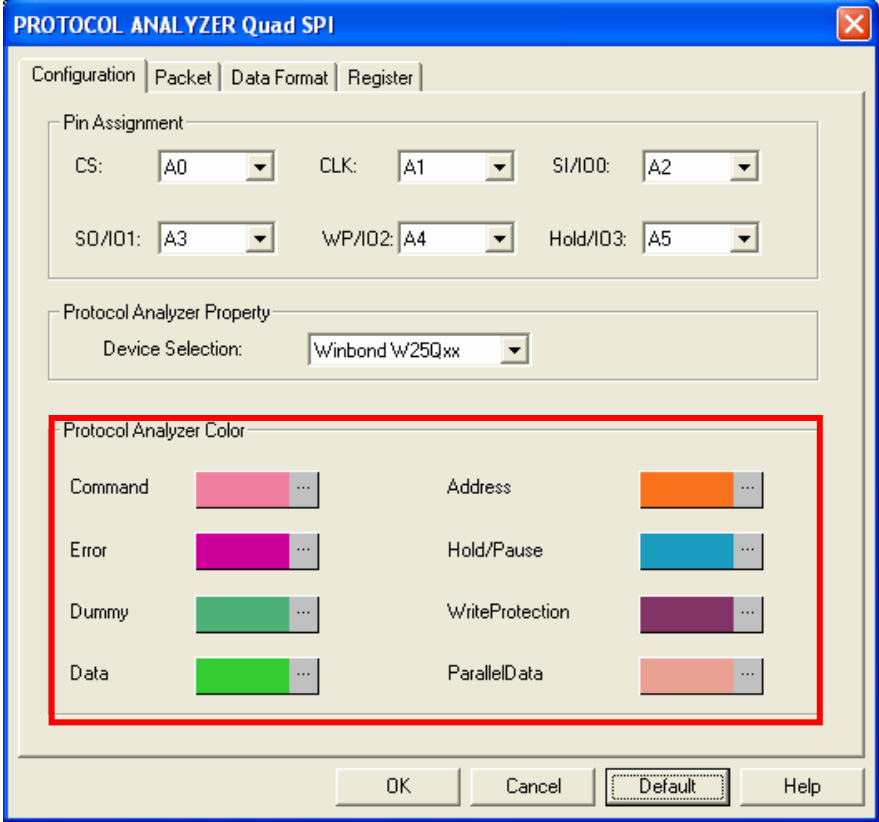
**STEP 4.** Set the Pin Assignment. They are A0-A5 respectively by default.



**STEP 5.** Do the Device Selection. It is Winbond W25Qxx by default.

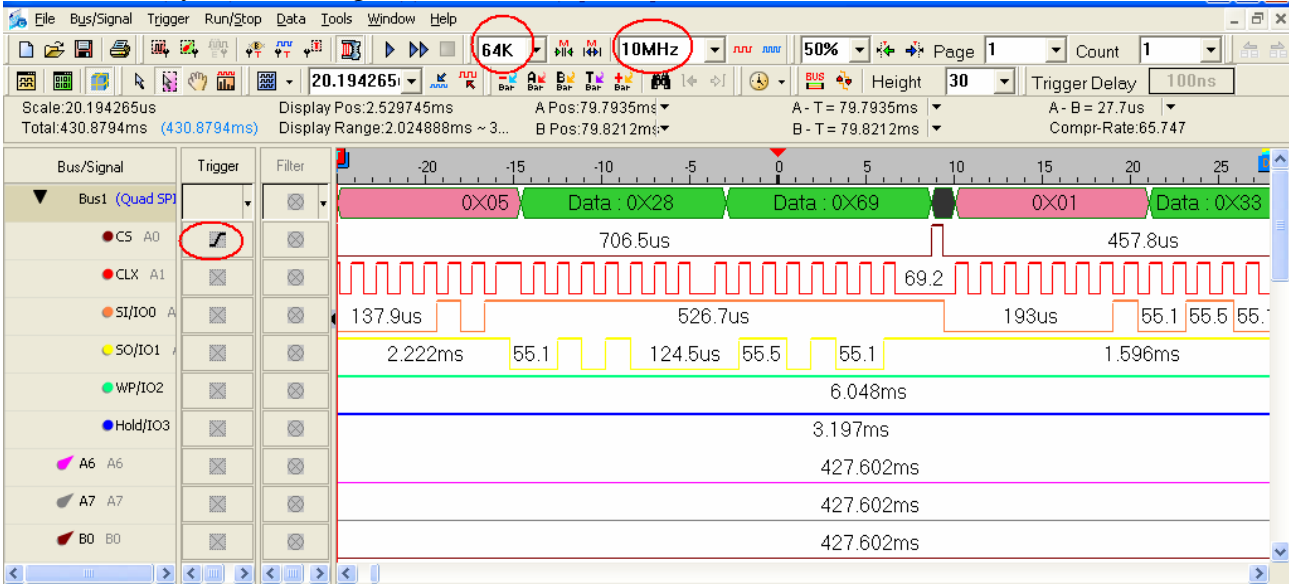


**STEP 6.** Set the Protocol Analyzer Color



**STEP 7.** Following pictures show the completion of the protocol analyzer decoding and the packet list. The trigger condition is set as Rising Edge, the memory depth is 64K and the sampling frequency is 10MHz (the sampling frequency should be more than four times higher than the signal to be tested).

Protocol Analyzer Decoding



Packet List

